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SpaceWire Engineering Excellence

SpaceWire Link Analyser Mk2: Debugging a SpaceWire Hardware Link Fault

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The SpaceWire Link Analyser Mk2 is designed specifically to support the testing and debugging of SpaceWire systems. This application note provides an example of debugging a SpaceWire hardware link fault using a Link Analyser Mk2, saving the engineer significant time and effort in the process.

Scenario

During the development of a piece of SpaceWire equipment we need to debug a hardware problem with a SpaceWire link. When the unit under test is connected to another piece of SpaceWire equipment we observe a link disconnect suggesting an error on the link. For example, the UUT may be connected to a STAR-Dundee Router-USB Mk2 in which case the disconnect is indicated by the link status LED flashing red.

Debugging with the Link Analyser Mk2

In this scenario the Link Analyser Mk2 is used to confirm that the link is operating as expected prior to the fault, identify the type of errors occurring in the link and allow the specific error to be targeted to capture the trace at the time of the fault. The trace can then be inspected for the cause of the fault. The following sections describe how this can be done.

Test Setup

Connect the Link Analyser between the two pieces of equipment using the two SpaceWire interfaces on the front panel. Connect it to a host PC via USB. The diagram below illustrates this configuration. The Link Analyser Mk2 will allow the traffic to flow on the link between the equipment as before while allowing the traffic to be monitored unobtrusively.



Application Note

View Link Statistics

Once connected, launch the SpaceWire Link Analyser Mk2 software on the host PC. The Link Analyser Mk2 immediately starts to record link statistics. To view the link statistics open the Status Counters display. This shows the number of errors and characters recorded and the current signalling rate. This view can quickly show if the link is operating as expected i.e. the signalling rate is correct and the number and types of characters recorded are as expected. It is also a good immediate indicator of link faults. The screenshot below shows in this example parity errors have occurred on End A and a disconnect error on End A and End B. The signalling rate is zero because at this stage the link has been disconnected. The status counters display can help indicate errors or characters of importance that can be used to trigger the Link Analyser Mk2 and narrow the search for the fault (see below for more on triggering). Alongside the link statistics are graphs that illustrate the character and error activity over time. In this example we see data, EOP, FCT and NULL characters (represented by the blue, salmon, cyan and light grey lines) are flowing in both directions of the link up until a disconnect error occurs on End A and End B (represented by the red line).



Status Counter Display

Capture the SpaceWire Traffic

In order to view and debug the SpaceWire traffic between the two pieces of equipment we must first capture it. SpaceWire link traffic is captured when a user defined trigger sequence is detected on the link. The trigger can be configured to occur on a range of different events including time-codes, data characters, control characters and errors. In this scenario we want to identify what error(s) led to the disconnect therefore it may be useful to trigger on a character sequence error, escape error, credit error, parity error, disconnect error or all errors. The status counter display has however already indicated the cause of the disconnect as a parity error on End A. To confirm this we want to analyse the link traffic surrounding the parity errors. The trigger is set to occur when a parity error is detected on End A as shown below. In this example the trigger is set to occur when a single event is detected as there is no need to do anything else. We can however configure the trigger to occur when a complex sequence of multiple different events is detected.





Trigger on Parity Error

The Link Analyser passively buffers the SpaceWire traffic on the link in anticipation of the trigger. When the trigger occurs, traffic before and after the trigger is captured. The memory reserved for before and after the trigger is configurable. In this example it is known that a parity error occurs before the disconnect. Therefore we need only reserve a small amount of memory to capture the trace post trigger to confirm the cause of the disconnect error is the parity error. The majority of the memory is used to store the trace before the trigger to try and capture the cause of any faults.

| SpaceWire Link Analyser Mk2 (Serial Number: 35100030) | | |
|---|--------------------|--------------------|
| Eile View Eind Settings Trigger Help | | |
| Trigger Sequence | | |
| Trigger Options | | |
| Data <u>C</u> omparators | | |
| Packet Count Comparator | | |
| Timecode Comparator | | |
| Character Enable | - | |
| External Trigger Op Trigger Options | - | |
| Start Trigger | | |
| Stop Trigger | | |
| 0.0 ns 🔻 | | |
| Character Memory Trigger Position (in %) | | |
| | | |
| | | |
| 0 20 40 60 80 100 | | |
| Bit-Stream Memory Trigger Position (in %) | | |
| | | |
| | | |
| 0 20 40 60 80 100 | | |
| | | |
| OK Cancel | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| | | |
| Idle | End A: 200.002 MHz | End B: 200.002 MHz |

Trigger Options



It is possible to enable and disable recording of specific character types. Disabling the recording of one character type means more memory is available to those remaining enabled character types. As disabled character types are not recorded it also means they are not present in the displays, effectively filtering the character types. For these reasons, in this example, NULL control code recording is disabled.



Disable NULL Control Code Recording

Analyse the SpaceWire Traffic

Once captured, we can inspect the SpaceWire link traffic using the character, packet and bitstream displays. The character display shows the characters, events and errors captured. In this trace we can see the trigger has occurred on the parity error as setup in the trigger sequence. There are a number of indicators of this in the "Time From Trigger" column: the "Time From Trigger" value equals zero, the cell is outlined in red representing a marker and the column background colour is red (blue indicates before trigger, red after). This is shortly followed by disconnect errors on both ends of the link, confirming the cause of the disconnect is a parity error introduced by the unit under test.





Character Display

In this scenario the parity error has already been identified as the cause of the disconnect but in a different scenario it may not be immediately obvious what the problem is. The Link Analyser software can display up to one million events and so searching for problems manually can be time consuming. To save time and effort the character display has a search feature to aid navigation and debugging that can be used to find specific events and errors.

| SpaceWire Lini | Analyser Mk2 (| Serial Numb | ber: 35100030) - | [~~ Not Saved ~~] | | | | | |
|------------------|-----------------|-------------------|------------------|-----------------------|--------------------|--------------|-----|-----------------|-----------------|
| File View Find | Settings Tri | gger <u>H</u> elp |) | | | | | | |
| | Search Characte | er Display | | | | | | | |
| | Coareb Deeket F |)ionloy | | | | | | | |
| Time From Trigge | | nspiay | A Event | End A Error | End A Delta | End B Even | t F | End B Error E | nd B Delta |
| -1.590 µs | 50 ns | | | | | NCHAR [E1] | 1 | 50 | ns 🔺 |
| -1.540 µs | 50 ns | | NULL | | 200 ns | NCHAR [C2 |] | 50 | ns |
| -1.480 µs | 60 ns | 1 | | | | NCHYD L84 | 1 | 60 | ns |
| -1.470 µs | 10 ns | | Search Characte | r Display | | 8 | | 10 | ns |
| -1.360 µs | 110 ns | | | | | | | | |
| -1.270 µs | 90 ns | NC | Events | -rrors Sources | | | | 20 | 10 ns |
| -1.210 µs | 60 ns | NC | Lysins I | odurces | | | | | |
| -1.170 µs | 40 ns | NC | | | | COR EOR | | | |
| -1.110 µs | 60 ns | NC | All Ello | | equence E | scape-LOF | | 16 | 10 ns |
| -1.070 µs | 40 ns | NC | Parity E | rror 🛛 🔄 EOP Seq | uence 📃 Es | scape-EEP | | 40 | /ns |
| -1.020 µs | 50 ns | NC | Tx Cred | lit Error 🔲 EEP Sea | uence 🗆 Es | cane-Escane | | | |
| -970 ns | 50 ns | NC | | | | scape-Locape | | | |
| -920 ns | 50 ns | NC | Rx Cred | dit Error 🛛 🗌 Time-Co | de Sequence 🛛 🔄 Di | sconnect | | | |
| -900 ns | 20 ns | | | | | | | 17 | '0 ns |
| -850 ns | 50 ns | | | | | | | 50 | ins |
| -760 ns | 90 ns | | | | | | | 90 | ins |
| -710 ns | 50 ns | | | | Previous | Next | | 50 | ins ins |
| -660 ns | 50 ns | | | | | | · | 50 | ins |
| -610 ns | 50 ns | | · | | _ | | | 50 | ns |
| -560 ns | 50 ns | | | | | NCHAR [47] | | 50 | ns |
| -510 ns | 50 ns | | | | | NCHAR [8E | | 50 | ns |
| -460 ns | 50 ns | | NULL | | 200 ns | NCHAR [1C | | 50 | ns |
| -400 ns | 60 ns | | | | | NCHAR [39 | | 60 | ns |
| -390 fts | 70 nc | | POT | | 140.00 | EOP | | 10 |) IIS |
| -320 fis | 270.00 | Neu | TOT INT INT | | 140 NS | | | | |
| 0.00 | 270 IIS | INCH | NK [04] | | 270115 | | | | |
| 20.00 | 20 ns | | | | 20 ns | | | | |
| 70 ne | 40 ns | NCH | | TANTTERKO | 40 ns | | | | |
| 120 ns | 50 ns | NCH | AR [JE] | | 50 ns | | | | |
| 140 ns | 20 ns | NCH | FOR | | 20 ns | | | | |
| 840 ns | 700 ns | NCH | AR (851 | | 700 ns | | | | |
| 880 ns | 40 ns | NCH | IAR [7E] | | 40 ns | | | | |
| Character Displ | ay Packet Dis | splay Bit | t-Stream Display | (| | | 1 | | |
| Complete | | | | | | | | End A: 0.000 Hz | End B: 0.000 Hz |

Search Character Display for Parity Error



The packet display provides a packet level view of the captured data. In this scenario we can see the link errors (indicated by the red cell) have affected two packets on End A: one is missing an EOP marker and another is missing the packet header.

| | ngs <u>T</u> rigger <u>H</u> elp | | | | |
|----------------------------|----------------------------------|---|------------------|---------------------|-------------|
| Time From Trianes | Time Della | End 4 | Ford & Dollar | Ford D | End D Dalla |
| Time From Trigger | Time Delta | End A | End A Delta | End B | End B Deita |
| 3.360 µs | 50 ns | Cargo Size: 7 bytes | 50 ns | | |
| 3.020 µs | 340 ns | EUP | 340 ns | | 050 |
| .900 µs | 120 hs | | | Header: 81 | 050 HS |
| 2.830 µs | 70 hs | | | Cargo Size: 7 bytes | 70 hs |
| .510 µs | 320 ns | | 050 | EOP | 320 ns |
| .370 µs | 140 ns | Header: 82 | 050 ns | | |
| .300 µs | 70 hs | cargo Size: / bytes | 70 hs | | |
| .980 µS | 320 flS | EUP | 320 NS | Wardawa 00 | 670 = 2 |
| .840 µs | 140 ns | | | Header: 82 | 670 ns |
| .790 µs | 50 hs | | | Cargo Size: 7 bytes | 50 hs |
| 470 µs | 320 hs | H | 740 | BUP | 320 hs |
| 270 µs | 200 ns | Header: 83 | /10 ns | | |
| 210 µs | 60 hs | Cargo Size: 7 bytes | 60 ns | | |
| Juns | 310 hs | EOP | 310 hs | | 740 |
| bu ns | 140 ns | | | Header: 83 | 710 ns |
| 10 ns | 50 ns | | | Cargo Size: 7 bytes | 50 ns |
| 90 ns | 320 ns | | 0.50 | EOP | 320 ns |
| Uns | 340 ns | Header: 84 | 850 ns | | |
| ns | 50 ns | Cargo Size: 0 bytes | 50 ns | | |
| ns | | | | | |
| 0 ns | 70 ns | Missed Header | 70 ns | | |
|) ns | | Cargo Size: 2 bytes | | | |
| 40 ns | 70 ns | EOP | 70 ns | | |
| 40 ns | 700 ns | Header: 85 | 700 ns | | |
| 80 ns | 40 ns | Cargo Size: 7 bytes | 40 ns | | |
| 200 µs | 320 ns | EOP | 320 ns | | • |
| Expand All Collapse All | Data View | Integer Bit Width S 8 bit (Byte) A 8 bit (Byte) | O 32 Bit (DWord) | Byte Settings | 8 |
| Character Display | acket Display Bit-Stre | am Display | | | |
| | | | | | |

Packet Display

The bit-stream display provides a graph of the data and strobe signals captured on both SpaceWire links. The screenshot of the bit-stream display below shows the cause of the parity errors in this scenario. Between the marker A (the purple vertical line) and marker B (the orange vertical line) a simultaneous transition is highlighted on the data and strobe signals on End A.



Bit-Stream Display



Inspecting these views and comparing expected SpaceWire link traffic with actual traffic helps trace the cause of SpaceWire link problems. In this example the character display has confirmed the cause of the disconnect is a parity error, the packet display has shown those packets affected by the errors and the bit-stream display has shown the parity errors are a result of a simultaneous transition.

Conclusion

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When debugging a SpaceWire hardware link problem, using a Link Analyser Mk2 allows the engineer to inspect the link traffic; target specific event sequences and filter information, and therefore enhances their ability to solve the problem quickly and efficiently.

In this application note we have seen how the Link Analyser is connected and how to set a trigger to capture SpaceWire link traffic at the most appropriate time. Once captured, the Link Analyser's displays let us inspect and debug the traffic flowing in each direction at the character, packet and bit-stream level.

This document only briefly describes some of the Link Analyser's capabilities in relation to a specific problem. The Link Analyser Mk2 is provided with context sensitive help and a user manual that comprehensively describes the Link Analyser Mk2.

For more information please visit our website at <u>www.star-dundee.com</u> or contact us at <u>enquiries@star-dundee.com</u>.

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